

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	7496439
<b>Application Number:</b>	10510567
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	9020
<b>Title of Invention:</b>	Circuit board device for information apparatus, multilayered module board and navigation system
<b>First Named Inventor/Applicant Name:</b>	Hirohisa Miyazawa
<b>Customer Number:</b>	23911
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<b>Filer Authorized By:</b>	Stephen W. Palan
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<b>Application Type:</b>	U.S. National Stage under 35 USC 371

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Foreign Reference	55488USB4.pdf	367171 85b3c307e7b29477929dbeea1bd183c339c2c879	no	8

### Warnings:

### Information:

2	NPL Documents	55488USC1.pdf	119612 9234801b18be2dbf7fa1d8f056815dec346b1141	no	3
<b>Warnings:</b>					
<b>Information:</b>					
3	Foreign Reference	55488USB3.pdf	1089293 f75d12cb2251dc23eb93dabf53cc232ec84797f7	no	18
<b>Warnings:</b>					
<b>Information:</b>					
4	Transmittal Letter	55488USIDS.pdf	225967 677432fef0c631c832cdc96301f1d19e0f493624	no	4
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			1802043		
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